

ParMA

Parallel Programming for Multi-core Architectures

- Improve the performance of conventional HPC applications
- Enable the advent of power-intensive innovative embedded applications
- Exploit fully the power of multi-core architectures

PARTICIPATING PARTNERS



United Kingdom





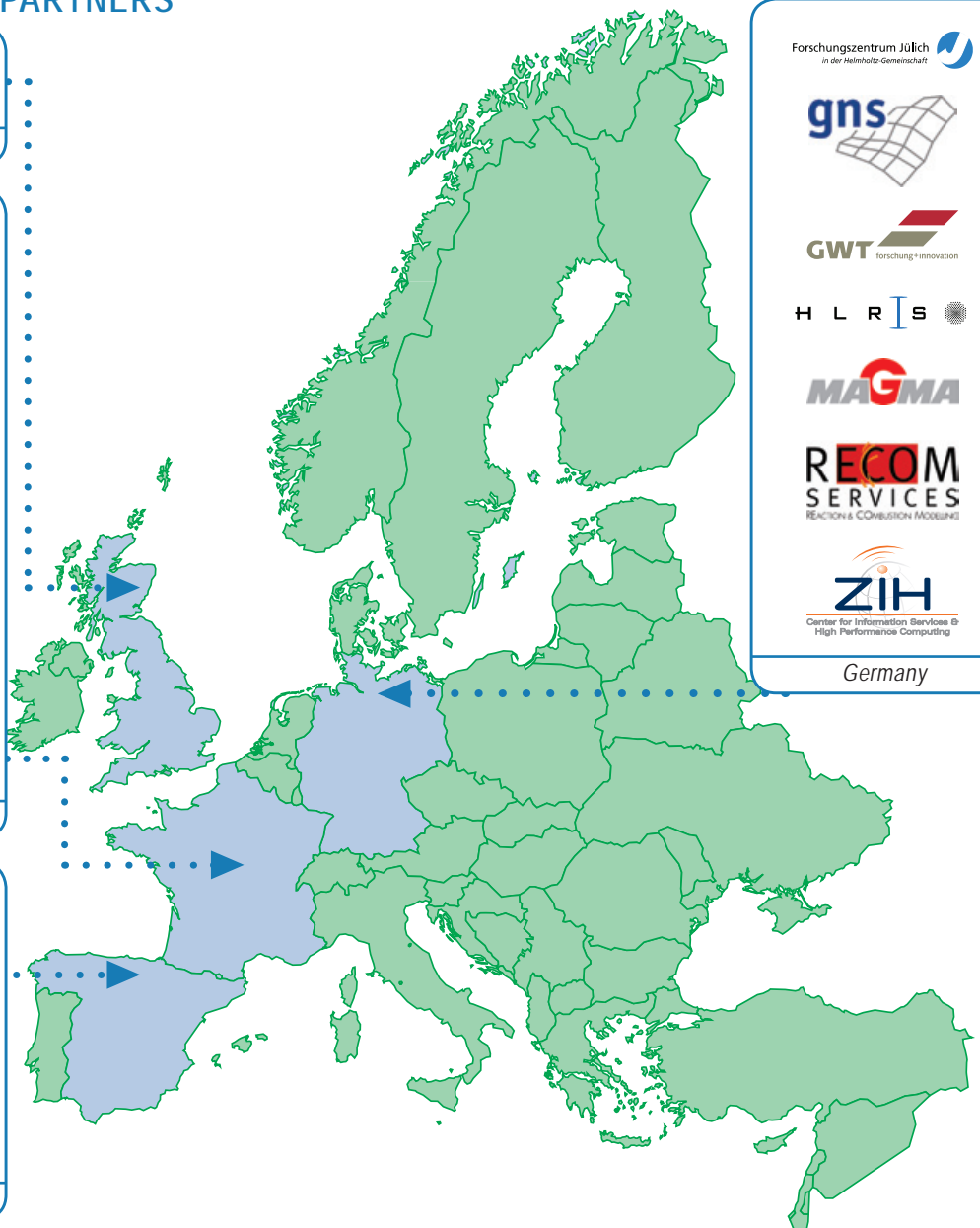



France





Spain










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